# **Coq to C Translation with Partial Evaluation**

Akira Tanaka

tanaka-akira@aist.go.jp National Institute of Advanced Industrial Science and Technology (AIST) Tsukuba, Ibaraki, Japan

# Abstract

Coq proof assistant can be used to prove various properties of programs written in the Gallina language. It is also possible to translate Gallina programs to OCaml programs. However, OCaml is not suitable for low-level programs. Therefore, we are developing a Coq plugin for Gallina to C translation. This plugin transforms functions written in Gallina into a form as close to C as possible within Gallina. This transformation includes partial evaluation, which improves execution efficiency and eliminates polymorphism and dependent types. We can easily verify in Coq that this transformation does not change the execution result, and thus it is highly reliable. And Gallina functions after this transformation can be easily translated to C.

# CCS Concepts: • Software and its engineering $\rightarrow$ Compilers; Software verification.

*Keywords:* Coq, C, Gallina, translator, compiler, verification, partial evaluation, tail recursion

#### **ACM Reference Format:**

Akira Tanaka. 2021. Coq to C Translation with Partial Evaluation. In Proceedings of the 2021 ACM SIGPLAN Workshop on Partial Evaluation and Program Manipulation (PEPM '21), January 18– 19, 2021, Virtual, Denmark. ACM, New York, NY, USA, 18 pages. https://doi.org/10.1145/3441296.3441394

# 1 Introduction

We are developing a Gallina to C translator, Codegen<sup>1</sup> plugin for Coq proof assistant [10], which can generate low-level C programs. The initial target of Codegen is libraries that handle complex data structures such as succinct data structures [8]. To achieve this, we must be able to easily use various low-level features such as 64 bit integers, built-in functions for CPU instructions, hardware specific types such as for SIMD registers, and so on. In other words, the goal

<sup>1</sup>https://github.com/akr/codegen

PEPM '21, January 18–19, 2021, Virtual, Denmark

ACM ISBN 978-1-4503-8305-9/21/01...\$15.00 https://doi.org/10.1145/3441296.3441394 is to be able to use features that are available for usual C programs from Gallina programs.

Gallina is a purely functional ML-like language provided by Coq and is used for programs and proofs. However it shares several features with many imperative languages including C.

- function definition
- function invocation
- conditional
- variable declaration and its initialization
- variable reference
- recursive function

With these features, basic programming is possible. Therefore, it is possible to translate a Gallina subset of these features into a C subset to achieve translation without overheads.

But there are also gaps between Gallina and C.

- 1. Gallina has type polymorphism, but C does not.
- 2. Gallina has dependent types, but C does not.
- 3. Gallina can use any evaluation strategy, but C is strict.
- 4. Gallina uses curried functions, but C does not.
- 5. Gallina has first-class functions, but C does not.
- 6. Gallina's data types (such as Peano's naturals defined as an inductive type) are suitable for proof but its naive implementation is too inefficient. C has efficient data types such as 64 bit integer.
- 7. All Gallina programs terminate but C does not.
- 8. Gallina uses only immutable values but C uses mutable values.
- 9. Gallina does not require to release memory but C requires.
- 10. Gallina does not have a loop, but C does.

In this paper, we describe how we used partial evaluation to address the first two of these gaps in the translator from Gallina to C. Gallina achieves type polymorphism and dependent types as type computation. We eliminate them by computing them statically.

We also describe several Gallina transformations to make Gallina functions suitable to translate to C. The result of these transformations (including partial evaluation) is easily verified by Coq because they are convertible.

The 3rd and 4th gaps relate evaluation strategy. Codegen generates C functions that implement the strict evaluation of Gallina terms. It does not violate Gallina semantics because Gallina can use any evaluation strategy. We assume that a

Publication rights licensed to ACM. ACM acknowledges that this contribution was authored or co-authored by an employee, contractor or affiliate of a national government. As such, the Government retains a nonexclusive, royalty-free right to publish or reproduce this article, or to allow others to do so, for Government purposes only.

 $<sup>\</sup>circledast$  2021 Copyright held by the owner/author (s). Publication rights licensed to ACM.

Gallina function body is evaluated after all arguments have been given, just as in C. This means that the evaluation of a partial application evaluates the function position and the arguments but does not evaluate the body of the function.

Codegen eliminates tail recursion using goto to address the last gap. Codegen can generate a C function that contains multiple loops and nested loops from Gallina terms which contain fixpoints at non-tail positions and nested fixpoints. Codegen also supports mutually tail-recursive Gallina functions by generating a single C function that contains multiple function bodies. Although goto is not usable for indirect calls, direct calls are enough to represent loops.

Solutions to the other gaps are not the subject of this paper and are summarized in Appendix A.

Section 2 explains that Coq has a feature similar to partial evaluation but it has problems. Section 3 explains the whole structure of our translation. Section 4 explains the transformations in Gallina terms. Section 5 explains C code generation. Section 6 explains several examples. We describe related work in Section 7 and conclude in Section 8.

# 2 Partial Evaluation with Stock Coq?

Gallina syntax is shown in Figure 1. Gallina has conversion rules shown in Figure 2 to test the equality of terms. If two terms are confluent by the conversion rules, they are said to be convertible.

The six reductions in the conversion rules define the evaluation of Gallina. Since the reductions are strong normalization, any reduction can be applied to any subterm in any order. Coq also has a command to define a reduced term as a new constant.

These features are similar to partial evaluation. However, it has two problems: (1) the reductions can duplicate computations to produce inefficient programs; (2) Coq cannot replace a function and its static arguments with a specialized function.

#### 2.1 Reduction of pow

We can define a power function in Coq.

Fixpoint pow x y := match y with  

$$| 0 \Rightarrow 1$$
  
 $| S y' \Rightarrow x * pow x y'$   
end.

We can define the normal form of pow  $\times$  3 with reductions (beta, iota-match, iota-fix, and delta-global for the constant pow) using call-by-value strategy.

Definition pow3 x :=

Eval cbv beta iota delta [pow] in pow x 3.

This definition is the same as follows.

Definition pow3 x := x \* (x \* (x \* 1)).

The result has no recursive call and conditional. It should run efficiently.

Akira Tanaka

x	variable
С	constant
C c	constructor
Т	type
$\lambda x:T.t$	abstraction
t u	application
let $x := t : T$ in $u$	let-in
match $t_0$ with $(C_i \Rightarrow t_i)_{i=1h}$ end	conditional
$fix (f_i/k_i : T_i := t_i)_{i=1h}$ for $f_j$	fixpoint

Note:

- *u* represents a term as *t*.
  - y, z, and f represent a variable as x.
- U represents a type as T.
- We write  $(\cdots ((t u_1) u_2) \cdots u_n)$  as  $t u_1 \ldots u_n$ .
- k is an integer.  $k_i$  for fixpoint specify the decreasing argument for  $f_i$ .
- If it is unambiguous, we omit type annotations for the sake of simplicity. We also omit *k<sub>i</sub>* in fixpoints if they are not used.
- We omitted the elimination predicate (as-in-return clause of match-expression). It is not used in reductions.
- match-branches t<sub>i</sub> are functions that take the constructor members (constructor arguments without inductive type parameters). This formalism is taken from CIC [11].
- We omitted the detail of the types. Actual Gallina permits any Gallina term which evaluates to a type.

#### Figure 1. Gallina syntax.

But Coq has no feature to replace pow invocations in the environment. We cannot replace pow z 3 with pow3 z in functions already defined. If we have a function f that uses pow z 3 and generate OCaml definition of f using the Extraction plugin, the generated function uses pow, not pow3.

Another problem is that the reduction rules can duplicate computations.

Definition pow\_2x\_3 x :=

Eval cbv beta iota delta [pow] in pow (x + x) 3.

This is the same as follows.

Definition pow\_2x\_3 x :=
 (x + x) \* ((x + x) \* ((x + x) \* 1)).

This result contains 3 times of x + x. Thus, it is not as efficient as we want.

# 2.2 Verification of pow3

We can verify that pow x = 3 and pow  $3 \times are$  equal in Coq.

Goal ∀ x, pow x 3 = pow3 x. Proof. reflexivity. Qed.

This equality means that evaluation of them produces the same value. The reflexivity tactic confirms that pow x = 3 and pow  $3 \times 3$  are convertible.

 $\begin{array}{ll} \text{beta: } E[\Gamma] \vdash ((\lambda x. t) \ u) \triangleright \ t\{x/u\} & \text{delta-local: } \frac{(x \mathrel{\mathop:}= t) \in \Gamma}{E[\Gamma] \vdash x \mathrel{\mathop{\mapsto}} t} & \text{delta-global: } \frac{(c \mathrel{\mathop:}= t) \in E}{E[\Gamma] \vdash c \mathrel{\mathop{\mapsto}} t} \\ \text{zeta: } E[\Gamma] \vdash \text{let } x \mathrel{\mathop:}= t \ \text{in } u \triangleright u\{x/t\} \\ \text{iota-match: } \frac{E[\Gamma] \vdash C_j \ u_1 \ldots u_{p+m} : T \quad p \ \text{is the number of parameters of the inductive type } T}{E[\Gamma] \vdash \text{match } (C_j \ u_1 \ldots u_{p+m}) \ \text{with } (C_i \Rightarrow t_i)_{i=1\ldots h} \ \text{end} \mathrel{\mathop{\triangleright}} t_j \ u_{p+1} \ldots u_{p+m} \\ \text{iota-fix: } \frac{u_{k_j} = C \ u'_1 \ldots u'_m}{E[\Gamma] \vdash (\text{fix } (f_i/k_i \mathrel{\mathop:}= t_i)_{i=1\ldots h} \ \text{for } f_j) \ u_1 \ldots u_{k_j} \mathrel{\mathop{\triangleright}} t_j \{f_k/\text{fix } (f_i/k_i \mathrel{\mathop:}= t_i)_{i=1\ldots h} \ \text{for } f_k\}_{k=1\ldots h} \ u_1 \ldots u_{k_j} \\ \text{eta expansion: } \frac{E[\Gamma] \vdash t : \forall x : T. \ U}{E[\Gamma] \vdash t \mathrel{\mathop{\triangleright}} \lambda x : T. \ (t \ x)} \end{array}$ 

Note:

- The rules shown here are reductions, except the eta expansion.
- $t\{x/u\}$  means a term in which x in term t is replaced by u. This notation is taken from the Coq reference manual [11].
- Variables cannot conflict because Coq uses de Bruijn's indexes to represent variables.
- *E* is a global environment which is a list of global assumptions (*c*:*T*), global definitions (*c* := *t*:*T*), and inductive definitions (Ind [p] ( $\Gamma_I := \Gamma_C$ )).
- $\Gamma$  is a local context which is a list of local assumptions (x:T) and local definitions (x := t:T). The local assumptions represent variables bounded by outer abstractions and fixpoints. The local definitions represent variables bounded by outer let-in.
- If it is unambiguous, we omit type annotations in these definitions for the sake of simplicity.
- Iota-match reduces match @cons nat 1 nil with (nil  $\Rightarrow$   $t_1$ ) (cons  $\Rightarrow$   $t_2$ ) end to  $t_2$  1 nil because list has one parameter (p = 1) and cons has two members (m = 2).

Figure 2. Gallina conversion rules.

# **3** Translation from Gallina to C

We implement the Gallina to C translation with the following two kinds of translation phases.

- Convertible transformations
- C code generation

The former transforms Gallina functions in a convertible way. The latter translates Gallina functions to C functions.

We implement translation phases in the former as much as possible. This allows us to verify with Coq that these transformations do not change the result of the function.

A user can configure the translation as follows.

- constant, constructor and inductive type name mappings between Gallina and C (Gallina names are used as-is<sup>2</sup> by default)
- zero-argument constructors to translate without parenthesis (Codegen generates parenthesis by default but possible to translate true to true instead of true() and 0 to 0)
- code snippets to translate match-expression to switchstatement (they are generated from the inductive type and constructor names by default)
- which the arguments are static or dynamic (only type arguments are static by default)
- functions to inline (no functions to inline by default)
- which types are linear (no linear types by default)

## **4** Convertible Transformations

Codegen defines two constants with the convertible transformations: terms before and after the transformations.

For example, when a user specifies the second argument of pow static and Codegen find an invocation of pow with the actual argument 3, Codegen defines two constants<sup>3</sup>:

The convertible transformations generate the latter (prefixed with  $s_{-}$ ) from the former (prefixed with  $p_{-}$ ). Codegen verifies the convertibility of them when the latter is defined.

Codegen applies seven convertible transformations described in Section 4.2 to Section 4.8.

#### 4.1 Computation Size

We define the convertible transformations that do not duplicate computations. For this purpose, we define "computation size" as an approximation of running time<sup>4</sup>.

We define the computation size as the number of matchexpression evaluated at run time. The size estimates a loop count. Because a loop is represented by recursion in Gallina and recursive functions must use match-expression to obtain a subterm of the decreasing argument for a recursive call.

<sup>&</sup>lt;sup>2</sup>Invalid characters for a C identifier are replaced by \_.

<sup>&</sup>lt;sup>3</sup>We use longer names in actual implementation.

<sup>&</sup>lt;sup>4</sup>The computation size is not an approximation of code size. If we want to avoid exponential code bloat, we can disable delta-fun, iota-fix-var, and iota-fix-var'. This makes complex partial evaluation impossible but monomorphization is still possible.

zeta-arg: 
$$\frac{E[\Gamma] \vdash t_i : T_i \quad t_0 \text{ is not an application} \quad t_i \text{ is not a variable} \quad x_i \text{ is a fresh variable}}{E[\Gamma] \vdash t_0 x_1 \dots x_{i-1} t_i t_{i+1} \dots t_n \vdash \text{let } x_i := t_i : T_i \text{ in } t_0 x_1 \dots x_{i-1} x_i t_{i+1} \dots t_n}$$
zeta-item: 
$$\frac{E[\Gamma] \vdash t_0 : T_0 \quad t_0 \text{ is not a variable} \quad x_0 \text{ is a fresh variable}}{E[\Gamma] \vdash \text{match } t_0 \text{ with } (C_i \Rightarrow t_i)_{i=1\dots h} \text{ end} \vdash \text{let } x_0 := t_0 : T_0 \text{ in match } x_0 \text{ with } (C_i \Rightarrow t_i)_{i=1\dots h} \text{ end}}$$

Figure 3. V-reductions.

$$\begin{split} t &= x \mid c \mid C \mid T \mid \lambda x : T. \ t \mid \text{let } x := t : T \text{ in } u \\ &\mid \text{fix } (f_i / k_i : T_i := t_i)_{i=1...h} \text{ for } f_j \\ &\mid t \ x & \leftarrow (1) \\ &\mid \text{match } x \text{ with } (C_i \Rightarrow t_i)_{i=1...h} \text{ end} & \leftarrow (2) \end{split}$$

**Figure 4.** V-normal form restricts Gallina terms that (1) application arguments and (2) match items to variables.

#### 4.2 Inlining

Codegen inlines user-specified functions using delta-global reduction. (The target of specialization, pow in p\_pow3 for example, is inlined automatically.) This phase does not apply further reductions such as beta reduction.

Since delta-global is part of the conversion rules, this transformation is convertible.

We assume functions inlined are defined as terms formed as an abstraction or a fixpoint. In this case, inlining does not change the computation size because the evaluation of them does not cause an evaluation of match-expression.

#### 4.3 V-Normalization

Codegen transforms arguments of application and match items as variables as A-normal form [4]. We call the result of this transformation V-normal form. V-normal form permits more expressions at binding expressions in let-in and function positions in applications than A-normal form. Figure 3 shows V-reductions to generate V-normal form shown in Figure 4.

V-reductions introduce let-in expressions. V-reductions produce a convertible term because they are zeta expansion.

V-reductions move an expression from an application argument and a match item. This moves computations but does not change the computation size.

#### 4.4 S-Normalization

We define S-reductions in Figure 5 to simplify V-normal terms. S-reductions are the reduction rules for the partial evaluation without computation duplication.

S-reductions resemble the conversion rules. Especially, they are terminating because iota-fix-var (and iota-fix-var') has the same guard condition of iota-fix.

**beta-var.** The beta-var reduction is a restricted version of the beta reduction. It applies the beta reduction when a beta

redex occurs in the function position of a nested application that returns an inductive value. This avoids to reduce partial applications and follows our strict evaluation strategy that invokes a function with all arguments. The arguments of the beta-var redex are variables because of V-normal form. It does not duplicate computation because the argument does not contain a match-expression.

**delta-var.** The delta-var reduction is a restricted version of the delta-local reduction. It can be applied only when the local definition maps the variable to another variable. This happens when the outer let-in bounds the variable to another variable. Since a variable does not contain a matchexpression, delta-var reduction does not change the computation size.

**delta-fun.** The delta-fun reduction is another restricted version of the delta-local reduction. It can be applied only when the variable f occurs in the function position of an application,  $f y_1 \dots y_q$  (0 < q), and the bounded expression,  $t x_1 \dots x_p$  ( $0 \le p$ ), has no computation. The evaluation of the function t has no computation because there is a premise to avoid match-expression. The evaluation of the arguments,  $x_1 \dots x_p$  has no computation because they are just variables.  $t x_1 \dots x_p$  is a partial application because it is the function of  $f y_1 \dots y_q$  and 0 < q. The evaluation of  $t x_1 \dots x_p$  does not evaluate the function body of t because our strict evaluation strategy does not invoke the function of a partial application. Thus, the bounded expression has no computation and the delta-fun reduction does not change the computation size.

**zeta-flat.** The zeta-flat reduction commutes let-in expression to make any let-in expression does not bind a let-in expression. This reduction can be considered as a combination of zeta reduction to delete the let-in expression for x and zeta expansion to restore it in the different position. The evaluation of the term before and after the reduction is both that evaluation of  $t_1$ ,  $t_2$ ,  $t_0$ . Thus, it does not change the computation size.

**zeta-app.** The zeta-app reduction moves a let-in binding from the function position of an application outside of the application. This reduction can be considered as a combination of zeta reduction and zeta expansion such as zeta-flat reduction. This reduction moves *t* but does not change the computation size.

$$\begin{array}{l} \text{beta-var:} & \frac{0 < n \quad E[\Gamma] \vdash (\lambda x. t) \ y_1 \dots y_n \models T \quad T \text{ is an inductive type}}{E[\Gamma] \vdash (\lambda x. t) \ y_1 \dots y_n \models t \ x/y_1 \ y_2 \dots y_n} & \text{delta-var:} & \frac{(x \coloneqq y) \in \Gamma}{E[\Gamma] \vdash x \models y} \\ \text{delta-fun:} & \frac{0 \leq p \quad 0 < q \quad (f \coloneqq t x_1 \dots x_p) \in \Gamma \quad t \text{ is one of } x, c, C, \lambda x. u, \text{fix } (f_i/k_i \coloneqq t_i)_{i=1\dots h} \text{ for } f_j}{E[\Gamma] \vdash f \ y_1 \dots y_q \models t \ x_1 \dots x_p \ y_1 \dots y_q} \\ \text{zeta-flat:} E[\Gamma] \vdash \text{let } y \coloneqq (\text{let } x \coloneqq t_1 \text{ in } t_2) \text{ in } t_0 \models \text{let } x \coloneqq t_1 \text{ in } (\text{let } y \coloneqq t_2 \text{ in } t_0) \\ \text{zeta-app:} E[\Gamma] \vdash (\text{let } x_0 \coloneqq t \text{ in } u) \ x_1 \dots x_n \models \text{let } x_0 \coloneqq t \text{ in } (u \ x_1 \dots x_n) \\ \text{iota-match-var:} & \frac{(x \coloneqq C_j \ y_1 \dots y_{p+m} \colon T) \in \Gamma \quad p \text{ is the number of parameters of the inductive type } T}{E[\Gamma] \vdash \text{match } x \text{ with } (C_i \Longrightarrow t_i)_{i=1\dots h} \text{ for } f_j \ x_{i_1} \dots x_{i_n} \mapsto T \ E(x_{i_1} \vdash x_{i_1} \vdash t_{i_1} \vdash t_$$

Figure 5. S-reductions.

**iota-match-var.** The iota-match-var reduction reduces a match-expression to one of its branches if the constructor for the match item is known. Since the match item is always a variable in V-normal form, the premise of this reduction examines the local definition. We require that the local definition contains a constructor application with arguments as variables. This requirement makes it possible to use the constructor members in the reduced expression without duplicating computation. This reduction can be considered as a combination of delta-local reduction to make the match item constructor form and iota-match reduction. It decreases the computation size because it removes the evaluation of the match-expression at run time.

**iota-fix-var.** The iota-fix-var reduction reduces a fixpoint application that returns an inductive value. We require all arguments, not only until the decreasing arguments, to avoid reducing partial applications. This reduction introduces letin bindings for the fixpoints to preserve V-normal form. Since the decreasing argument is a variable in V-normal form, the premise examines the local definition. Thus, this reduction can be considered as a combination of delta-local reduction to make the decreasing argument constructor form, iota-fix reduction, delta-local expansion to restore the decreasing argument, and zeta expansions to extract the fixpoints substituted by the iota-fix reduction. Since the introduced let-in bindings have no computation (because they are fixpoints without arguments), this reduction does not change the computation size. **iota-fix-var'.** The iota-fix-var' reduction is the same as iota-fix-var reduction but reuses already bounded fixpoints. This reduction is optional. This reduction can be considered as a combination of the conversion rules the same as iota-fix-var except that delta-local expansions are used instead of the last zeta expansions. It has the property the same as iota-fix-var reduction except that the size of intermediate terms are smaller.

#### 4.5 Call Site Replacement

Codegen replaces a function and its static arguments with a specialized function. When new values for static arguments are found, a new specialized function (before transformations prefixed by  $p_{-}$ ) is defined in the global environment.

The values of static arguments are obtained by normalizing the actual static arguments (variables) using the reductions in the conversion rules. Codegen requires that these normal forms have no free variables.

This transformation does not start the convertible translations for other functions and does not define simplified functions (prefixed with  $s_{-}$ ) for the other functions. Thus, Codegen can transform one function at a time.

This transformation can be considered as delta-global expansion addition to the reductions for normalizing the static arguments. If a dynamic argument exists before a static argument such as pow, beta expansion is also used before the delta-global expansion: when the normal form of three is 3, pow z three is transformed to (fun  $x \Rightarrow pow x 3$ ) z and (fun  $x \Rightarrow pow x 3$ ) is replaced with the constant p\_pow3.

zeta-del:  $\frac{x \text{ does not occur in } u}{E[\Gamma] \vdash \text{let } x := t \text{ in } u \triangleright u}$ 

Figure 6. Unused let-in deletion.

Although delta-global expansion introduces new constant in the global environment, convertibility test still works because it can use delta-global reduction.

It does not change the computation size because the function position is a constant and the static arguments are variables and they do not contain match-expression.

## 4.6 Unused let-in Deletion

This transformation deletes unused let-in expressions as shown in Figure 6. It is applied after call site replacement because call site replacement removes variable references such as three in the example in Section 4.5.

This reduction is a restricted zeta reduction.

This reduction removes the binding expression t in let-in expression. Therefore, it can decrease the computation size when an evaluation of t evaluates a match-expression. But it does not increase the computation size.

#### 4.7 Argument Completion

Codegen applies eta expansion to avoid partial applications because C has no partial application. This makes all C variables<sup>5</sup> explicit in Gallina. It simplifies the arguments generated by eta expansion by moving the arguments into a body of let-in expression and beta-var reduction. Thus, this transformation can be considered as a combination of eta expansion, zeta-app reduction, and beta-var reduction.

It also transforms match-branches into nested abstractions corresponding to the members of constructors. This transformation is also eta expansion.

This transformation does not change the computation size because we apply eta expansion only when it does not delay evaluation. Since a function body is evaluated after all arguments are given, we can apply eta expansion for the function body (top-level functions and functions for fixpoints). If a constructor has k members, the corresponding match-branch is evaluated with k arguments so we can apply k times of eta expansion for the branch.

The detail of this transformation is shown in Appendix B.

#### 4.8 C Variable Allocation

This transformation allocates C variables for Gallina variables. It generates unique names that are valid in C. It may allocate one C variable for multiple Gallina variables when they are always bound to the same value. For example, y is always the same as z in match x with  $tt \Rightarrow fun y \Rightarrow y end z$ . We

formalized it in Appendix  ${\mathbb C}$  to generates a variable mapping from Gallina to C.

Codegen implements this allocation by renaming variables in Gallina terms. This "renaming" does not change de Bruijn's indexes but changes variable names. Therefore, we can inspect the renaming by printing the result of the convertible transformations. This is the reason that we define this as one of the convertible transformations. This transformation does not affect the evaluation of Gallina terms.

#### 4.9 Well-Typedness of the Transformations

The transformations produce convertible terms because they are combinations of the conversion rules. However, several transformations use the reverse of the conversion rules. Vnormalization uses zeta expansion. S-normalization uses zeta expansion and delta-local expansion. Call site replacement uses delta-global expansion and beta expansion. Argument completion uses zeta expansion. Since the conversion rules restore the original term, they do not break convertibility. Furthermore, zeta expansion, delta-local expansion, and delta-global expansion produces a well-typed term from a well-typed term. They replace an expression with a new variable or constant which is convertible to the original expression. Since the Gallina typing rules do not distinguish convertible terms, they produce a well-typed term. The beta expansion can produce an ill-typed term for a dependently typed term [11] but we focus non-dependent terms after S-normalization.

# 5 C Code Generation

Codegen generates C code from the result of the convertible transformations. However Codegen gives up code generation if the result still contains type computations<sup>6</sup> or closure generation. We describe the subset of Gallina that can be translated to C in Appendix  $D^7$ .

This translation is easy because:

- No complex subexpressions because of V-normal form (like A-normal form)
- No type computations and closure generation
- No partial applications and C variables are already allocated

The convertible transformations may retain lambda abstractions not only at the top-level of a function. Codegen can generate C code for them as far as arguments are given lexically outside of them. For example, Codegen can generate C code for match x with tt  $\Rightarrow$  fun y  $\Rightarrow$  y end z. Since

<sup>&</sup>lt;sup>5</sup>The temporary variables for parallel assignments are the exception.

<sup>&</sup>lt;sup>6</sup>The type computation includes type polymorphism, polymorphic recursion, and dependent types

<sup>&</sup>lt;sup>7</sup>This is the intermediate representation for Codegen, not the definition of the source Gallina terms for Codegen. We do not define the source Gallina terms for Codegen because the convertible transformations may remove arbitrary complex terms. For example, Codegen can handle match u with true  $\Rightarrow 0 \mid false \Rightarrow t$  end for any t, as far as u is reduced to true. It is not possible to determine that without actually reducing u.

Codegen does not support closures yet, lambda abstractions that have no such actual arguments are not supported.

Codegen needs one more complicated task, tail recursion elimination. Codegen guarantees it because Gallina has no loops and we need a reliable way to describe loops that do not consume the stack. Since C compilers do not guarantee tail recursion elimination, Codegen must do it.

We show the code generation in Figure 7 for non-tail position and Figure 8 for tail position. Figure 9 defines auxiliary functions for them. Top-level function generation is described in Appendix E.

Codegen translates a recursive call in a tail position using goto. This is the traditional translation method and allows us to generate a loop in a tail position.

We also generate a loop in a non-tail position. We want this feature to generate loops for inlined small higher-order functions such as fold\_left. Such functions are used not only in tail positions. Thus, we generate a loop from a fixpoint application in a non-tail position. However, not all of them can be a loop.

Whether a fixpoint application in a non-tail position can be a loop is determined by TR in Figure 10. TR determines that a fixpoint can be a loop if all recursive calls to the functions bounded by the fixpoint are translated to goto. This is equivalent to a C compiler determining that a recursive function cannot be inlined, but a non-recursive function can be inlined, even if it contains goto.

Codegen can generate a C function that contains multiple Gallina functions. It is used to generate mutually recursive functions with goto. See Appendix E for details.

# 6 Translation Examples

## 6.1 Convertible Transformations without Duplicating Computations

The convertible transformations do not duplicate computations. Therefore, we can specialize  $pow_2x_3$  in Section 2.1 properly. The convertible transformations in Codegen define  $s_pow_2x_3$  as follows:

Definition s\_pow\_2x\_3 v1\_x := let v2\_n := v1\_x + v1\_x in let v3\_n := 0 in let v4\_n := S v3\_n in let v5\_n := v2\_n \* v4\_n in let v6\_n := v2\_n \* v5\_n in v2\_n \* v6\_n.

The addition occurs only once and is not duplicated.

#### 6.2 Specialized Function Application Generation

Codegen generates a specialized function that invokes other specialized functions. Assume the following definition and the second argument of pow is static.

Definition f x y := pow x 3 + pow y 3.

The convertible transformation for f generates  $p_pow3$  and  $s_f$  as follows. ( $p_f$  is not generated because f has no static arguments.)

```
Definition p_pow3 x := pow x 3.
Definition s_f v1_x v2_y :=
    let v3_n := p_pow3 v1_x in
    let v4_n := p_pow3 v2_y in
    v3_n + v4_n.
```

s\_pow3 can be generated by applying the convertible transformations to p\_pow3.

```
Definition s_pow3 v1_x :=
  let v2_n := 0 in
  let v3_n := S v2_n in
  let v4_n := v1_x * v3_n in
  let v5_n := v1_x * v4_n in
  v1_x * v5_n
```

Codegen generates C functions for pow3 and f from  $s_pow3$  and  $s_f$  as in Figure 11. This result shows that Codegen generates a specialized function that invokes another specialized function: the invocation of pow in f is translated to the invocation of pow3. This is different from Coq reductions with the Extraction plugin described in Section 2.1.

These functions depend on a C implementation of the inductive type and primitive functions. If a user wants to implement nat by uint64\_t, the implementation in Figure 12 is possible<sup>8</sup>.

## 6.3 Monomorphization of List.rev

The convertible transformations are usable for monomorphization (type polymorphism elimination). Since Gallina is a dependently typed language, types are first-class values and they can be passed as arguments. If a type is passed as an argument and it is used for the type of a later argument, the function is polymorphic. Thus, a beta reduction can eliminate a type argument. (If a fixpoint has a type argument, we cannot monomorphize it. This means Codegen cannot eliminate polymorphic recursion.)

Coq has a list reversal function rev in the standard library. It is defined as follows  $^{10}. \,$ 

```
Definition rev := fun (A : Type) \Rightarrow
fix rev (l : list A) : list A :=
match l with
| nil \Rightarrow nil
| x :: l' \Rightarrow rev l' ++ x :: nil
end.
```

<sup>&</sup>lt;sup>8</sup>nat and uint64\_t are not isomorphic because nat has infinitely many elements and uint64\_t has a finite number of elements. We provide another Coq plugin, monadification<sup>9</sup> [9] to translate a function to use option monad. We can use it to verify that this mismatch does not cause problems when overflow does not occur.

<sup>&</sup>lt;sup>9</sup>https://github.com/akr/monadification

<sup>&</sup>lt;sup>10</sup>The standard library uses Section to define this term.

 $A_K[[t / x_1 ... x_n]]$  generates C code for  $t x_1 ... x_n$  in a non-tail position. The result expression is passed to K. K(e) = "v = e;" in simple situations.

$$\begin{split} & A_{K}[x/] = K("x") \\ & A_{K}[x/x_{1}...x_{n}] = "passign(fvars'[x], x_{1}...x_{n}) \text{ goto entry}_{x};" \quad n > 0 \land x \text{ is bounded by a fixpoint } \land x \in TR \\ & A_{K}[x/x_{1}...x_{n}] = K("x(y_{1},...,y_{n},x_{1},...,x_{n})") \quad n > 0 \land x \text{ is bounded by a fixpoint } \land x \notin TR \\ & A_{K}[c/x_{1}...x_{n}] = K("c(x_{1},...,x_{n})") \quad n \geq 0 \\ & A_{K}[c/x_{1}...x_{n}] = K("c(x_{1},...,x_{n})") \quad n \geq 0 \\ & A_{K}[[x/x_{0}/x_{1}...x_{n}] = K("c(x_{1},...,x_{n})") \quad n \geq 0 \\ & A_{K}[[x/x_{0}/x_{1}...x_{n}] = K("c(x_{1},...,x_{n})") \quad n \geq 0 \\ & A_{K}[[x/x_{0}/x_{1}...x_{n}] = A_{K}[[t/x_{0}x_{1}...x_{n}]] \quad where \quad K'(e) = "x = e;" \\ & A_{K}[[xx, t/x_{1}x_{2}...x_{n}]] = A_{K}[[t/x_{2}...x_{n}]] \quad where \quad K'(e) = "x = e;" \\ & A_{K}[[xx, t/x_{1}x_{2}...x_{n}]] = A_{K}[[t/x_{2}...x_{n}]] \quad where \quad x:T \\ & "switch (swfunc_{T}(x)) \{ \\ & \dots \\ & caselabel_{C_{1}}: \\ & y_{11} = get\_member_{C_{1}}(x); \dots; y_{INM_{C_{1}}} = get\_member_{C_{I}NM_{C_{I}}}(x); \\ & A_{K}[[f_{1}/x_{1}...x_{n}]] \\ & break; \\ & \dots \\ & y" \\ & A_{K}[[f_{1}/x_{1}...x_{n}]] \\ & break; \\ & \dots \\ & gENBODY_{K}^{AT}[[f_{1}x(f_{i} := t_{i})_{i=1...h} \text{ for } f_{j}]] \\ & exit\_f_{i}" \\ & A_{K}[[f_{1}x(f_{i} := t_{i})_{i=1...h} \text{ for } f_{j}]] \\ & exit\_f_{i}" \\ & GENBODY_{K}^{AT}[[f_{1}x(f_{i} := t_{i})_{i=1...h} \text{ for } f_{j}]] \\ & getShopDY_{K}^{AT}[[f_{1}x(f_{i} := t_{i})_{i=1...h} \text{ for } f_{j}]] \\ & skip\_f_{i}" \\ \end{array}$$

Note:

- "..." means a string. A string can contain characters in typewriter font and expressions starting in italic or roman font. The former is preserved as-is. The latter embeds the value of the expression (with name translation from Gallina to C).
- Gallina types, constants, and constructors have corresponding (user-configurable) C names and they are implicitly translated. Gallina variables are translated by the mapping defined in Section 4.8.
- $TR = TR[t]_n$  where the translating function is defined as Definition c := t and t is an *n*-arguments function.
- NM<sub>C</sub> is the number of the members of the constructor C (the number of arguments without the parameters for the inductive type): NM<sub>C</sub> = m if C :  $T_1 \rightarrow \cdots \rightarrow T_p \rightarrow T_{p+1} \rightarrow \cdots \rightarrow T_{p+m} \rightarrow T_0$  and  $T_0$  is an inductive type which has p parameters.
- *swfunc*<sub>T</sub>, *caselabel*<sub>C<sub>i</sub></sub>, and *get\_member*<sub>C,i</sub> are defined by a user to translate match-expressions for the inductive type T.
- passign $(y_1 \dots y_n, x_1 \dots x_n)$  is a parallel assignment. It is translated to a sequence of assignments to assign  $x_1 \dots x_n$  into  $y_1 \dots y_n$ . It may require temporary variables.
- $y_1, \ldots, y_o$  are the outer variables of the fixpoint.
- We do not define  $A_K[[\lambda x. t / ]]$  because we do not support closures yet.
- Actual Codegen generates GENBODY<sup>AN</sup> [] in a different position to avoid the label skip\_ $f_j$  and goto skip\_ $f_j$ ;

Figure 7. Translation to C for a non-tail position.

 $B_K[t / x_1 \dots x_n]$  generates C code for  $t x_1 \dots x_n$  in a tail position. The result expression is passed to K. K(e) = "return e;" in simple situations.

$$\begin{split} & B_{K} \|x/\| = K("x") \\ & B_{K} \|x/x_{1} \dots x_{n}\| = \text{``passign}(\text{fvars'} [x], x_{1} \dots x_{n}) \text{ goto entry}_{x}; \text{``} \quad n > 0 \land x \text{ is bounded by a fixpoint} \\ & B_{K} [x/x_{1} \dots x_{n}] = K("c(x_{1}, \dots, x_{n})") & n \ge 0 \\ & B_{K} [x_{0}/x_{1} \dots x_{n}] = B_{K} [t/x_{0} x_{1} \dots x_{n}] \\ & B_{K} [t x_{0}/x_{1} \dots x_{n}] = B_{K} [t/x_{0} x_{1} \dots x_{n}] \\ & B_{K} [let x := t_{1} \text{ in } t_{2}/x_{1} \dots x_{n}] = \text{``A}_{K'} [t_{1}/] B_{K} [t_{2}/x_{1} \dots x_{n}] \text{'``where } K'(e) = "x = e; " \\ & B_{K} [\lambda x. t/x_{1} x_{2} \dots x_{n}] = B_{K} [t/x_{2} \dots x_{n}] & (x \text{ and } x_{1} \text{ are mapped to the same C variable}) \\ & B_{K} [match x \text{ with } (C_{i} \Rightarrow \lambda y_{i1} \dots \lambda y_{i \text{ NM}_{C_{i}}} \cdot t_{i})_{i=1\dots h} \text{ end } / x_{1} \dots x_{n}]] = \text{ where } x : T \\ & \text{```switch } (swfunc_{T}(x)) \{ \dots \\ & caselabel_{C_{i}} : y_{i1} = get\_member_{C_{i}1}(x); \dots; y_{i \text{ NM}_{C_{i}}} = get\_member_{C_{i} \text{ NM}_{C_{i}}}(x); \\ & B_{K} [t_{i}/x_{1} \dots x_{n}] \\ & \dots \\ & y" \\ & B_{K} [t_{i}/x_{1} \dots x_{n}] = \text{```} \\ & B_{K} [t_{i}/x_{1} \dots x_{n}] = \text{```} \\ & gassign(fvars[t_{j}], x_{1} \dots x_{n}) \\ & GENBODY_{K}^{B} [fix (f_{i} := t_{i})_{i=1\dots h} \text{ for } f_{j}]" \end{aligned}$$

Note: We do not define  $B_K[[\lambda x. t / ]]$  because a tail position cannot be a function after the argument completion.

Figure 8. Translation to C for a tail position.

$$\begin{aligned} & \text{fvars}\llbracket t \rrbracket = \begin{cases} \text{"x; } \text{fvars}\llbracket u \rrbracket " \quad t = \lambda x. \ u \\ & \text{fvars}\llbracket t_j \rrbracket \quad t = \text{fix} \ (f_i := t_i)_{i=1...h} \text{ for } f_j \\ & \text{otherwise} \end{cases} \\ & \text{fvars}'\llbracket f_i \rrbracket = \text{fvars}\llbracket t_i \rrbracket \quad \text{for functions bounded by } \text{fix} \ (f_i := t_i)_{i=1...h} \text{ for } f_j \\ & \text{fvars}'\llbracket t_i \rrbracket = \begin{cases} \text{GENBODY}_K^{\text{AT}} \llbracket u \rrbracket \quad t = \lambda x. \ u \\ \text{"entry}_f : \text{ GENBODY}_K^{\text{AT}} \llbracket u \rrbracket \quad t = \text{fix} \ (f_i := t_i)_{i=1...h} \text{ for } f_j \\ & \text{for } i = j, 1, \dots, (j-1), (j+1), \dots, h \\ & A_K \llbracket t / \rrbracket \quad \text{otherwise} \end{cases} \end{aligned}$$

Note:

• fvars and fvars' returns a list of variables:  $x_1; \ldots; x_n$ ; For simplicity, we omit ";" if not ambiguous.

• "g(i)" for  $i = j_1, ..., j_n$  means " $g(j_1) ... g(j_n)$ ".

Figure 9. Auxiliary functions for translation to C.

 $TR[t]_n$  is the first element of  $RNT[t]_n$ .

 $(R, N, T) = \text{RNT}[t]_n$  classify variables in t assuming that it is called with n arguments as  $t x_1 \dots x_n$ :

R : tail-recursive fixpoint bounded functions that do not need to be real functions

N : free variables at non-tail positions of t

T: free variables at tail positions of t

"tail position" is extended to the function position of the application at a tail position. *R* distinguishes fixpoint bounded functions translatable without actual functions (but with goto) or not.

 $\begin{aligned} \text{TR}[\![t]\!]_n &= R \quad \text{where} \quad (R, N, T) = \text{RNT}[\![t]\!]_n \\ \text{RNT}[\![t]\!]_n &= \\ & \begin{pmatrix} (\varnothing, \oslash, \{x\}) & t = x \\ (\varnothing, \oslash, \oslash) & t = c \lor t = C \\ (R, N \cup \{x\}, T) & t = u x \quad \text{where} \quad (R, N, T) = \text{RNT}[\![u]\!]_{n+1} \\ (R_1 \cup R_2, N_1 \cup T_1 \cup N_2 - \{x\}, T_2 - \{x\}) & t = \text{let } x := t_1 \text{ in } t_2 \\ \text{where} \quad (R_1, N_1, T_1) = \text{RNT}[\![t_1]\!]_0 \quad (R_2, N_2, T_2) = \text{RNT}[\![t_2]\!]_n \\ (\bigcup_{i=1}^h R_i, \bigcup_{i=1}^h N_i, \bigcup_{i=1}^h T_i) & t = \text{match } x \text{ with } (C_i \Rightarrow t_i)_{i=1...,h} \text{ end } \text{ where } (R_i, N_i, T_i) = \text{RNT}[\![t_i]\!]_{\text{NM}_{C_i}} + n \\ (R, N - \{x\}, T - \{x\}) & t = \lambda x. \ u \land n > 0 \quad \text{where} \quad (R, N, T) = \text{RNT}[\![u]\!]_{n-1} \\ (R, (N \cup T) - \{x\}, \varnothing) & t = \lambda x. \ u \land n = 0 \quad \text{where} \quad (R, N, T) = \text{RNT}[\![u]\!]_{NA_u} \quad (\text{closure}) \\ (\bigcup_{i=1}^h R_i \cup \{f_1, \ldots, f_h\}, & t = \text{fix} (f_i := t_i)_{i=1...,h} \text{ for } f_j \land \\ \bigcup_{i=1}^h N_i - \{f_1, \ldots, f_h\}, & n = \text{NA}_t \land (\bigcup_{i=1}^h N_i \cap \{f_1, \ldots, f_h\} = \varnothing) \\ \bigcup_{i=1}^h N_i - \{f_1, \ldots, f_h\}, & t = \text{fix} (f_i := t_i)_{i=1...,h} \text{ for } f_j \land (\text{real function, maybe closure}) \\ (\bigcup_{i=1}^h R_i, & t = \text{fix} (f_i := t_i)_{i=1...,h} \text{ for } f_j \land (\text{real function, maybe closure}) \\ (\bigcup_{i=1}^h R_i, & t = \text{fix} (f_i := t_i)_{i=1...,h} \text{ for } f_j \land (\text{real function, maybe closure}) \\ (\bigcup_{i=1}^h (N_i \cup T_i) - \{f_1, \ldots, f_h\}, & \neg (n = \text{NA}_t \land (\bigcup_{i=1}^h N_i \cap \{f_1, \ldots, f_h\} = \varnothing)) \\ \varnothing) & \text{where} \quad (R_i, N_i, T_i) = \text{RNT}[\![t_i]\!]_{\text{NA}_{t_i}} \end{aligned}$ 

Note:

nat v3\_n; nat v4\_n;

 $v3_n = pow3(v1_x);$ 

 $v4_n = pow3(v2_y);$ 

}

return add(v3\_n, v4\_n);

• NA<sub>t</sub> is the number of arguments of t: NA<sub>t</sub> = m if  $t : T_1 \to \cdots \to T_m \to T_0$  and  $T_0$  is an inductive type.

• The variables in *t* are unique. Codegen uses de Bruijn's indexes for *N* and *T*; the variables renamed by Section 4.8 for *R*.

Figure 10. Detection of tail recursive fixpoint bounded functions.

```
static nat pow3(nat v1_x) {
    nat v2_n; nat v3_n; nat v4_n; nat v5_n;
    v2_n = 0();
    v3_n = S(v2_n);
    v4_n = mul(v1_x, v3_n);
    v5_n = mul(v1_x, v4_n);
    return mul(v1_x, v5_n);
}

static nat f(nat v1_x, nat v2_y) {
    Wa can managemention and but the convertible transformed and the server is the the server is
```

We can monomorphize rev by the convertible transformations as Figure 16. s\_rev\_bool is a monomorphic function without type arguments.

Codegen generates a C function as Figure 17 for it. This function depends on the implementation of bool type (bool type), list bool type (list\_bool type; lb\_nil and lb\_cons for constructors; lb\_is\_nil, lb\_head, and lb\_tail for matchexpression implementation), and the implementation of app (++ operator) for list bool type (app\_bool). See Appendix F for details.

Figure 11. pow3 and f generated by Codegen.

```
Fixpoint sprintf_type (fmt : string) : Type := match fmt with
  | EmptyString \Rightarrow buffer
  | String "%"%char (String "d"%char rest) \Rightarrow nat \rightarrow sprintf_type rest
  | String "%"%char (String "b"%char rest) \Rightarrow bool \rightarrow sprintf_type rest
  | String "%"%char (String "s"%char rest) \Rightarrow string \rightarrow sprintf_type rest
  | String "%"%char (String _ rest) ⇒ sprintf_type rest
  | String "%"%char EmptyString \Rightarrow buffer
  | String _ rest \Rightarrow sprintf_type rest end.
Fixpoint sprintf (buf : buffer) (fmt : string) : sprintf_type fmt := match fmt return sprintf_type fmt with
  | EmptyString \Rightarrow buf
  | String "%"%char (String "d"%char rest) \Rightarrow fun (n : nat) \Rightarrow sprintf (buf_addnat buf n) rest
  | String "%"%char (String "b"%char rest) \Rightarrow fun (b : bool) \Rightarrow sprintf (buf_addbool buf b) rest
  | String "%"%char (String "s"%char rest) \Rightarrow fun (s : string) \Rightarrow sprintf (buf_addstr buf s) rest
  | String "%"%char (String ch rest) \Rightarrow sprintf (buf_addch (buf_addch buf "%") ch) rest
  | String "%"%char EmptyString \Rightarrow buf_addch buf "%"%char
  | String ch rest \Rightarrow sprintf (buf_addch buf ch) rest end.
```

Figure 13. sprintf definition using dependent type.

```
Definition s_sprintf_x_eq_nat v1_buf v2_n :=
  let v3_b := false in let v4_b := false in let v5_b := false in let v6_b := true in
  let v7_b := true in let v8_b := true in let v9_b := true in let v10_b := false in
  let v11_a := Ascii v3_b v4_b v5_b v6_b v7_b v8_b v9_b v10_b in
  let v12_b := true in let v13_b := false in let v14_b := true in let v15_b := true in
  let v16_b := true in let v17_b := true in let v18_b := false in let v19_b := false in
  let v20_a := Ascii v12_b v13_b v14_b v15_b v16_b v17_b v18_b v19_b in
  let v21_b := buf_addch v1_buf v11_a in let v22_b := buf_addch v21_b v20_a in
  let v23_b := buf_addnat v22_b v2_n in v23_b
```

• Ascii is the constructor of ascii type defined in the standard library of Coq.

• v11\_a and v20\_a are the characters "x" and "=" (0x78 and 0x3d in ASCII).

Figure 14. sprintf specialized with respect to the format string "x=%d".

```
typedef unsigned char ascii;
#define Ascii(b0,b1,b2,b3,b4,b5,b6,b7) \
 ((b0) | (b1) << 1 | (b2) << 2 | (b3) << 3 | (b4) << 4 | (b5) << 5 | (b6) << 6 | (b7) << 7)
static buffer sprintf_x_eq_nat(buffer v1_buf, nat v2_n) {
    bool v3_b; bool v4_b; bool v5_b; bool v6_b; bool v7_b; bool v8_b; bool v9_b; bool v10_b; ascii v11_a;
    bool v12_b; bool v13_b; bool v14_b; bool v15_b; bool v16_b; bool v17_b; bool v18_b; bool v19_b; ascii v20_a;
    buffer v21_b; buffer v22_b; buffer v23_b;
    v3_b = false; v4_b = false; v5_b = false; v6_b = true; v7_b = true; v8_b = true; v9_b = true; v10_b = false;
    v11_a = Ascii(v3_b, v4_b, v5_b, v6_b, v7_b, v8_b, v9_b, v10_b);
    v12_b = true; v13_b = false; v14_b = true; v15_b = true; v16_b = true; v17_b = true;
    v18_b = false; v19_b = false; v20_a = Ascii(v12_b, v13_b, v14_b, v15_b, v16_b, v17_b, v18_b, v19_b);
    v21_b = buf_addch(v1_buf, v11_a); v22_b = buf_addch(v21_b, v20_a); v23_b = buf_addnat(v22_b, v2_n);
    return v23_b;
```

```
}
```

• ascii type and Ascii macro are a user-defined implementation of ascii type.

Figure 15. C code generation of sprintf specialized with respect to the format string "x=%d".

```
Akira Tanaka
```

```
Definition p_rev_bool := rev bool.
Definition s_rev_bool :=
  fix rev_bool (v1_1 : list bool) : list bool :=
  match v1_1 with
  | ni1 ⇒ lb_ni1
  | v2_x :: v3_1_ ⇒
    let v4_1 := rev_bool v3_1_ in
    let v5_1 := lb_ni1 in
    let v6_1 := lb_cons v2_x v5_1 in
    p_app_bool v4_1 v6_1
  end
```

Figure 16. The monomorphized rev function.

```
static list_bool rev_bool(list_bool v1_1)
{ bool v2_x; list_bool v3_1_; list_bool v4_1;
    list_bool v5_1; list_bool v6_1;
    switch (lb_is_nil(v1_1))
    { default: return lb_nil;
      case 0: v2_x = lb_head(v1_1);
        v3_1_ = lb_tail(v1_1);
        v4_1 = rev_bool(v3_1_);
        v5_1 = lb_nil;
        v6_1 = lb_cons(v2_x, v5_1);
        return app_bool(v4_1, v6_1);
    } }
```

Figure 17. Generated C code for rev bool.

#### 6.4 Dependent Type Elimination of sprintf

Gallina can use dependent types to define functions which type depends on (non-type) arguments. For example, sprintf in Figure 13 is a function that the second argument fmt defines types of subsequent arguments. The types of subsequent arguments are computed by sprintf\_type.

Codegen can specialize sprintf function to eliminate dependent types. Codegen specializes sprintf with respect to the format string "x=%d" as in Figure 14. The result does not contain dependent types. The result is very long because all bits of all characters in the format string are bound by variables. Figure 15 shows the C function generated for the specialized sprintf function.

sprintf takes a buffer as the first argument and returns a buffer that formatted string is appended. buffer type is expected to be declared as linear and the buf\_addch function modifies the buffer destructively. See Appendix G for details.

# 7 Related Work

The Extraction plugin [6] is a standard plugin of Coq. It translates Gallina to OCaml, Haskell, and Scheme. It translates one Gallina function to one function in a target language and has no feature to specialize functions. When a Gallina function uses a dependent type which is not typable in OCaml, Obj.magic is used. This requires a uniform data representation where all values can be represented by a single type (single word, typically) at run time. Codegen does not have this requirement and any specific representation is usable for each inductive type.

CertiCoq [1] and Œuf [7] are compilers for Gallina to generate executable code using CompCert [5]. They use a uniform data representation and have no specialization.

A-normal form [4], K-normal form [2], and our V-normal form restricts application arguments as variables. They also restrict condition expressions as variables. A-normal form prohibits a let-in and conditional expression in a let-in binding but K-normal form and V-normal form permit it. Anormal form restricts function position with values (constant, variable, lambda abstraction) and primitive operations. K-normal form restricts function position with variables (including letrec-bounded functions). V-normal form permits in function position any V-normal term including let-in, conditional expression, and fixpoint. This permissibility of V-normal form is important to represent a loop using an application which function position is a fixpoint.

Continuation Passing Style (CPS) is another choice for an intermediate language of compilers. CPS and V-normal form restricts arguments as variables. The naive CPS translation produces many administrative redexes that can be reduced at compile time [3]. Our V-reduction restricts arguments a term as variables and S-reduction simplifies the term. The difference between them is V-reduction does not introduce extra constructs if an argument is already a variable and Sreduction reduces redexes not only administrative redexes.

# 8 Conclusion

We described that conversion rules-based partial evaluation is possible and its result is easily verifiable in Coq. The partial evaluation is usable for monomorphization and dependent type elimination. We formalized the partial evaluation as reductions over a subset of Gallina term, V-normal form.

Codegen implements the partial evaluation and generates C functions. We defined several transformations to make a result of the partial evaluation close to C to ease C code generation. These transformations are also convertible as the partial evaluation and its result is easily verifiable.

The C code generation in Codegen guarantees tail recursion elimination. It eliminates tail recursion in a tail position and also eliminates tail recursion in a non-tail position if possible.

# Acknowledgments

This research was partially supported by the JSPS Bilateral Joint Research (Number: 20203001)/Inria AYAME Program Project FLAVOR.

# A Summary of Solutions to the Gaps between Gallina and C

- Gallina has type polymorphism, but C does not.
   → Codegen uses partial evaluation to eliminate type polymorphism if possible.
- Gallina has dependent types, but C does not.
   → Codegen uses partial evaluation to eliminate dependent types if possible.
- Gallina can use any evaluation strategy, but C is strict.
   → Codegen applies the strict evaluation strategy to Gallina terms.
- Gallina uses curried functions, but C does not.
   → Codegen applies the semantics that evaluates a function body after all arguments are given.
- Gallina has first-class functions, but C does not.
   → We have a plan to implement restricted closures (downward funarg).
- Gallina's data types (such as Peano's naturals defined as an inductive type) are suitable for proof but its naive implementation is too inefficient. C has efficient data types such as 64 bit integer.

 $\rightarrow$  Codegen is data representation agnostic. A user can customize the implementation of inductive types.

- All Gallina programs terminate but C does not.
   → Our target is library functions and most of them terminate.
- Gallina uses only immutable values but C uses mutable values.

 $\rightarrow$  Local variables are modifiable at tail recursive calls which are translated to assignments and goto. Also, Codegen has a linearity checker for destructive updates on heap structures safely. It checks the linearity of variables of user-specified types: one variable is used exactly once. It guarantees that destructive update is invisible from Gallina program.

• Gallina does not require to release memory but C requires.

 $\rightarrow$  The linearity checker guarantees to consume a linear variable. It can be used to force a user to invoke a memory freeing function.

Gallina does not have a loop, but C does.
 → Codegen implements reliable tail recursion elimination.

# **B** Details of Argument Completion

Figure 18 explains the detail of argument completion.

# **C** Formalized C Variable Allocation

Figure 19 and Figure 20 formalizes C variable allocation. Figure 19 defines the syntax of a mapping from Gallina variables to C variables. Figure 20 generates a variable mapping for a Gallina term.

# D The Gallina Subset for C Code Generation

Our C code generator can generate a C function if  $E[] \vdash_f t$ . Figure 21 defines  $E[\Gamma] \vdash_f t$  and  $E[\Gamma] \vdash_b t$ .  $E[\Gamma] \vdash_f t$  determines that *t* is valid function for our C code generator.  $E[\Gamma] \vdash_b t$ determines that *t* is valid body for our C code generator.

# **E** Top-Level C Function Generation

Figure 22, Figure 23, and Figure 24 explains the top-level function generation.

Figure 22 generates C definitions for a Gallina function that needs multiple C functions. Figure 23 generates a C function for a Gallina function that needs only one C function. Figure 24 chooses them. Assuming a Gallina function c is defined as Definition c := t, it needs multiple functions when a function bounded by a fixpoint in t is called as a function (not goto) and c itself is not usable to call the function.

For example, half function defined as follows is mutually defined with uphalf function. half invokes uphalf in a tail position but uphalf invokes half in a non-tail position.

```
Definition half :=

fix half n :=

match n with 0 \Rightarrow 0 | S m \Rightarrow uphalf m end

with uphalf n :=

match n with 0 \Rightarrow 0 | S m \Rightarrow S (half m) end

for half.
```

Codegen generates half as a single function that contains the bodies of both functions. This is because all recursive calls are translatable to goto or a function call to the toplevel function (half): half invokes uphalf using goto; uphalf invokes half using a usual function call.

```
static nat half(nat v1_n) {
  nat v2_m, v3_n, v4_m, v5_n;
  switch (v1_n) {
    case 0: return 0;
    default: v2_m = pred(v1_n);
             v3_n = v2_m;
             goto entry_uphalf;
  }
  entry_uphalf:
  switch (v3_n) {
    case 0: return 0;
    default: v4_m = pred(v3_n);
             v5_n = half(v4_m);
             return succ(v5_n);
  }
}
```

- F[[*t*]] considers t to be a top-level function or a closure-generating expression. F transforms *t* to be a nested abstraction expression that takes all arguments. Fixpoint expressions are allowed outside or between the abstractions.
- BR [[t]]<sub>m,q</sub> transforms a branch of a match expression into a nested abstraction expression that takes constructor members. Fixpoint expressions are not allowed.
- $\mathbb{E}[t / x_1 \dots x_p]_q$  is a term convertible with  $t x_1 \dots x_p$  that does not contain a partial application.  $\mathbb{E}[t / x_1 \dots x_p]_q$  traverses *t* while tracking the arguments for *t* to find closure-generating expressions. The number of arguments given to *t* is p + q. The first *p* arguments are  $x_1 \dots x_p$  and they can be the argument of beta-var redex. The last *q* arguments cannot be the argument of beta-var redex.

$$\begin{split} \mathbb{F}\llbracket I \end{bmatrix} &= \begin{cases} \lambda x. \mathbb{F}\llbracket u \rrbracket & t = \lambda x. u \\ fix (f_i \coloneqq \mathbb{F}\llbracket t_i \rrbracket)_{i=1...h} \text{ for } f_j \quad t = fix (f_i \coloneqq t_i)_{i=1...h} \text{ for } f_j \\ \lambda x_1 \dots \lambda x_m. \mathbb{E}\llbracket t / x_1 \dots x_m \rrbracket_0 \quad \text{otherwise} \quad (\text{eta expansion}) \end{cases} \\ \text{where} \quad t : T_1 \to \cdots \to T_m \to T_0 \\ T_0 \text{ is an inductive type} \\ x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{BR}\llbracket t \rrbracket_{m,q} &= \begin{cases} \mathbb{E}\llbracket t / \rrbracket_q \qquad m = 0 \\ \lambda x. \mathbb{BR}\llbracket u \rrbracket_{m-1,q} \qquad m > 0 \land t = \lambda x. u \\ \lambda x_1 \dots \lambda x_m. \mathbb{E}\llbracket t / x_1 \dots x_m \rrbracket_q \quad \text{otherwise} \quad (\text{eta expansion}) \end{cases} \\ \text{where} \quad x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \mathbb{E}\llbracket t / x_1 \dots x_m \rrbracket_q \qquad t = x \land \neg (p = q = 0) \land r = 0 \\ \mathbb{E}\llbracket t / x_1 \dots x_m \text{ are fresh variables} \end{cases} \\ \mathbb{E}\llbracket t / x_1 \dots x_m \mathbb{E}\llbracket t / x_1 \dots x_m \rrbracket_q \qquad t = x \land \neg (p = q = 0) \land r > 0 \\ \mathbb{E}\llbracket t / x_1 \dots x_m \rrbracket_q \qquad t = x \land \neg (p = q = 0) \land r > 0 \\ \mathbb{E}\llbracket t / x_1 \dots x_m \rrbracket_q \qquad t = x \land \neg (p = q = 0) \land r > 0 \\ \mathbb{E}\llbracket t / x_1 \dots x_m \rrbracket_q \qquad t = x \land \neg (p = q = 0) \land r > 0 \\ \mathbb{E}\llbracket t / x_1 \land x_m \rrbracket_q \qquad t = \lambda x. u \land p = q = 0 \\ \mathbb{E}\llbracket t / x_1 \land x_m \rrbracket_q \qquad t = \lambda x. u \land p = q = 0 \\ \mathbb{E}\llbracket u \{x_1\} / x_2 \dots x_m \rrbracket_q \qquad t = \lambda x. u \land ((p = 0 \land q > 0) \lor r > 0) \\ \mathbb{E}\llbracket u \{x_1\} / x_2 \dots x_m \rrbracket_q \qquad t = \lambda x. u \land ((p = 0 \land q > 0) \lor r > 0) \\ \mathbb{E}\llbracket u / x_0 \land 1 \dots x_m \rrbracket_q \qquad t = \lambda x. u \land ((p = 0 \land q > 0) \lor r > 0) \\ \mathbb{E}\llbracket u / x_0 \land 1 \dots x_m \rrbracket_q \qquad t = \lambda x. u \land ((p = 0 \land q > 0) \lor r > 0) \\ \mathbb{E}\llbracket u / x_0 \land 1 \dots x_m \rrbracket_q \qquad t = \lambda x. u \land ((p = 0 \land q > 0) \lor r > 0) \\ \mathbb{E}\llbracket u / x_0 \land 1 \dots x_m \rrbracket_q \qquad t = \lambda x. u \land ((p = 0 \land q > 0) \lor r > 0) \\ \mathbb{E}\llbracket u / x_0 \land 1 \dots x_m \rrbracket_q \qquad t = \lambda x. u \land ((p = 0 \land q > 0) \lor r > 0) \\ \mathbb{E}\llbracket u / x_0 \land 1 \dots x_m \rrbracket_q \qquad t = \lambda x. u \land ((p = 0 \land q > 0) \lor r > 0) \\ \mathbb{E}\llbracket u / x_0 \land 1 \dots x_m \rrbracket_q \qquad t = \lambda x. u \land ((p = 0 \land q > 0) \lor$$

 $T_0$  is an inductive type

Note: This transformation assumes t is not dependently typed: no type terms and no dependent match-expressions.

Figure 18. Argument completion.

x : Gallina variable	
v : C variable	
$V = empty \mid x \mapsto v \mid V$	7;

Figure 19. Variable mapping from Gallina to C.

V

#### F list bool Implementation

list bool type can be implemented in C as Figure 25. This implementation uses malloc() but not free(). Thus a conservative GC is required.

# **G** buffer Implementation

buffer type can be implemented in C as Figure 26. unit type is also implemented.

make\_buffer allocates a buffer, buf\_addch adds a character to the buffer destructively, and free\_buffer deallocates the buffer.

buf\_addch and free\_buffer have side effects but it is not visible from Gallina as far as buffer type is used linearly. Codegen causes an error for a non-linear use of buffer type variables when buffer type is declared as linear.  $CV[[t / x_1 \dots x_n]]_V$  is the variable mapping of the variables declared in *t*.

 $x_1 \dots x_n$  are arguments for t. V is the variable mapping for variables declared outside.

$$CV[[t / x_1 \dots x_n]]_V = \begin{cases} empty & t = x \lor t = c \lor t = T \\ CV[[u / ]]_{V;M}; M & t = \lambda x. \ u \land n = 0 & \text{where } M = x \mapsto v \\ CV[[u / x_2 \dots x_n]]_{V;M}; M & t = \lambda x. \ u \land n > 0 & \text{where } M = x \mapsto V(x_1) \\ CV[[u / x_0 \ x_1 \dots x_n]]_V & t = u \ x_0 \\ CV[[t_1 / ]]_V; CV[[t_2 / x_1 \dots x_n]]_{V;M}; M & t = \text{let } x := t_1 \text{ in } t_2 & \text{where } M = x \mapsto v \\ (CV[[t_1 / x_1 \dots x_n]]_{V;M_1}; \dots; & t = \text{match } x \text{ with } (C_i \Rightarrow \lambda y_{i1} \dots \lambda y_{i \ NM_{C_i}}, t_i)_{i=1\dots h} \text{ end} \\ CV[[t_h / x_1 \dots x_n]]_{V;M_h}; & \text{where } M_i = y_{i1} \mapsto v_{i1}; \dots; y_i \ NM_{C_i} \mapsto v_i \ NM_{C_i} \\ (CV[[t_1 / ]]_{V;M}; \dots; CV[[t_h / ]]_{V;M}; M & t = \text{fix } (f_i := t_i)_{i=1\dots h} \text{ for } f_j \\ \text{where } M = f_1 \mapsto v_1; \dots; f_h \mapsto v_h \end{cases}$$

where  $v, v_i, v_{ij}$  are fresh C variables

Note: We consider Gallina variables unique.

#### Figure 20. C variable allocation.

 $E[\Gamma] \vdash_{\mathbf{b}} x$  $E[\Gamma] \vdash_{\mathrm{b}} c \qquad E[\Gamma] \vdash_{\mathrm{b}} C$  $E[\Gamma] \vdash_b t \quad E[\Gamma] \vdash x : T \quad T$  is a non-dependent inductive type  $E[\Gamma] \vdash_{\mathrm{b}} t x$  $E[\Gamma] \vdash_{b} t \quad E[\Gamma :: (x := t:T)] \vdash_{b} u \quad T \text{ is a non-dependent inductive type}$  $E[\Gamma] \vdash_{\mathbf{b}} \text{let } x := t : T \text{ in } u$  $E[\Gamma] \vdash x : T$  T is a non-dependent inductive type with p parameters:  $I u_1 \dots u_p$  $E[] \vdash C_i \ u_1 \dots u_p : T_{i1} \to \dots \to T_{i \operatorname{NM}_{C_i}} \to T \quad T_{ij} \text{ are non-dependent inductive types}$  $E[\Gamma :: (y_{i1}:T_{i1}) :: \cdots :: (y_{i \operatorname{NM}_{C_i}}:T_{i \operatorname{NM}_{C_i}})] \vdash_b t_i$  $E[\Gamma] \vdash_{\mathrm{b}} \mathrm{match} x \mathrm{with} (C_i \Rightarrow \lambda y_{i1} \dots \lambda y_{i \mathrm{NM}_{C_i}}, t_i)_{i=1\dots h} \mathrm{end}$  $E[\Gamma :: (x:T)] \vdash_b t$  T is a non-dependent inductive type  $E[\Gamma] \vdash_{\mathbf{b}} \lambda x : T. t$  $\frac{E[\Gamma] \vdash_{\mathrm{f}} \mathrm{fix} (f_i : T_i := t_i)_{i=1...h} \text{ for } f_j}{E[\Gamma] \vdash_{\mathrm{b}} \mathrm{fix} (f_i : T_i := t_i)_{i=1...h} \text{ for } f_j}$  $E[\Gamma] \vdash t : T \quad T$  is a non-dependent inductive type  $E[\Gamma] \vdash_b t$  $E[\Gamma] \vdash_{\mathrm{f}} t$  $E[\Gamma :: (x:T)] \vdash_{f} t$  T is a non-dependent inductive type  $E[\Gamma] \vdash_{\mathrm{f}} \lambda x : T. t$  $\frac{E[\Gamma :: (f_1:T_1) :: \cdots :: (f_h:T_h)] \vdash_f t_i}{E[\Gamma] \vdash_f fix (f_i:T_i := t_i)_{i=1...h} \text{ for } f_j}$ 

Figure 21. The Gallina subset for C code generation.

GENFUN<sup>M</sup>[[*c*]] translates the function (constant) *c* with one or more auxiliary functions. We assume *c* is defined as Definition c := t. The auxiliary functions  $f_1 \dots f_n$  are fixpoint bounded functions in *t* which are invoked as functions. We assume the types of them:

$$c: T_{01} \to \cdots \to T_{0m_0} \to T_{00}$$
  
$$f_i: T_{i1} \to \cdots \to T_{im_i} \to T_{i0} \qquad i = 1 \dots n$$
  
where  $T_{i0}$  are inductive types  $(i = 0 \dots n)$ 

The formal arguments of *c* are  $x_{01} \dots x_{0m_0} = \text{fvars}[t]$  and the formal arguments of  $f_i$  are  $x_{i1} \dots x_{im_i} = \text{fvars}'[f_i]$ .  $f_i$  invocation in C needs extra arguments,  $y_{i1} : U_{i1} \dots y_{io_i} : U_{io_i}$ , addition to the actual arguments in Gallina application because the free variables of the fixpoint should also be passed. If the free variables contain a function bounded by an outer fixpoint, the function itself is not passed but the free variables of the outer fixpoint are also passed. We iterate it until no fixpoint functions.

```
GENFUN^{M} [c] = "enum_entries[c]] arg_structdefs[c]] forward_decl[c]] entry_functions[c]] body_function[c]]"
enum entries [c] = "enum enum func c {func c, func f_1, \ldots, func f_n};"
arg_structdefs[c] = "main_structdef[c] aux_structdef[c]_1 ... aux_structdef[c]_n"
main_structdef [c] = "struct arg_c { T_{01} arg1; ... T_{0m_0} argm_0; };"
aux_structdef[c]_i = "struct arg_f_i { U_{i1} outer1; ... U_{io_i} outero_i; T_{i1} arg1; ... T_{im_i} argm_i; };"
forward_decl[[c]] = "static void body_function_c(enum enum_func_cg,void *arg,void *ret);"
entry functions [c] = "main function [c] aux function [c]_1 \dots aux function [c]_n"
main_function [c] = "static T_{00} c(T_{01} x_{01}, \dots, T_{0m_0} x_{0m_0}) \{
                              struct arg_c arg = {x_{01}, \ldots, x_{0m_0}}; T_{00} ret;
                              body_function_c(func_c,&arg,&ret);return ret;
                           }"
aux_function [c]_i =  "static T_{i0} f_i(U_{i1} y_{i1}, \dots, U_{i0_i} y_{i0_i}, T_{i1} x_{i1}, \dots, T_{im_i} x_{im_i})  {
                             struct arg f_i arg = {y_{i1}, \ldots, y_{i0i}, x_{i1}, \ldots, x_{imi}}; T_{i0} ret;
                             body_function_c(func_f_i,&arg,&ret); return ret;
                          }"
body_function[[c]] = "static void body_function_c(enum enum_func_c g, void *arg, void *ret) {
                              decls
                              switch (g) { aux_case [c]_1 \dots aux_case [c]_n main_case [c]  }
                              \text{GENBODY}_{K}^{\text{B}}[\![t]\!]
                          }"
aux_case [c]_i =  "case func_f_i:
                    y_{i1} = ((\text{struct} \arg_{f_i} *) \arg) - \text{outer1}; \dots; y_{io_i} = ((\text{struct} \arg_{f_i} *) \arg) - \text{outer}_i;
                    x_{i1} = ((\text{struct } \text{arg}_{f_i} *) \text{arg}) \rightarrow \text{arg}_1; \dots; x_{im_i} = ((\text{struct } \text{arg}_{f_i} *) \text{arg}) \rightarrow \text{arg}_{m_i};
                    goto entry f_i;"
main case [c] = \text{``default:};
                     x_{01} = ((\text{struct} \operatorname{arg}_c *) \operatorname{arg}) - \operatorname{arg}_1; \dots; x_{0m_0} = ((\text{struct} \operatorname{arg}_c *) \operatorname{arg}) - \operatorname{arg}_m;
where decls is local variable declarations for variables used in GENBODY<sup>B</sup><sub>K</sub>[[t]].
          K(e) = "*(T_{00}*)ret = e; return;"
```

Figure 22. Translation for a top-level function which is translated to multiple C functions.

GENFUN<sup>S</sup> [c] translates the function (constant) *c* to a single C function.

- GENFUN<sup>S</sup>  $[c] = "static T_0 c(fargs' [t]) \{ decls GENBODY_K^B [t] \}"$ 
  - where *c* is defined as Definition  $c: T_1 \to \cdots \to T_n \to T_0 := t$ .

 $T_0$  is an inductive type

*decls* is local variable declarations for variables used in GENBODY<sup>B</sup><sub>K</sub>[t] excluding fargs[t].

K(e) = "return e;"

$$\operatorname{fargs}[t] = \begin{cases} {}^{e}T x, \ \operatorname{fargs}[u]]^{n} & t = \lambda x : T. \ u \\ {}^{f}\operatorname{fargs}[t_{j}]] & t = \operatorname{fix} (f_{i} := t_{i})_{i=1...h} \ \operatorname{for} \ f_{j} \\ {}^{e}\hspace{-0.5mm}, {}^{e}\hspace{-0.5mm} \text{otherwise} \end{cases}$$

 $\operatorname{fargs}'[t] = \operatorname{fargs}[t]$  without the trailing comma

Figure 23. Translation for a top-level function which is translated to a single C function.

 $GENFUN[[c]] = \begin{cases} GENFUN^{M}[[c]] & t \text{ needs multiple functions} \\ GENFUN^{S}[[c]] & otherwise \end{cases}$ 

where c is defined as Definition c := t.



```
#include <stdlib.h>
#include <stdbool.h> /* bool, true, false */
struct list_bool_tag {
  bool head;
  struct list_bool_tag *tail;
};
typedef struct list_bool_tag *list_bool;
#define lb_nil NULL
static list_bool lb_cons(bool h, list_bool t) {
  list_bool c;
 if ((c = malloc(sizeof(*c))) == NULL)
   abort();
  c \rightarrow head = h;
  c \rightarrow tail = t;
  return c;
}
#define lb_is_nil(l) ((l) == lb_nil)
#define lb_head(l) ((l)\rightarrowhead)
#define lb_tail(l) ((l)→tail)
```

Figure 25. list bool implementation in C.

```
typedef bool unit; static const unit tt = true;
typedef struct {
 unsigned char *mem; size_t len; size_t max;
} *buffer;
buffer make_buffer(nat max) {
  buffer buf;
  if ((buf = malloc(sizeof(*buf))) == NULL) abort();
  if (max == 0) max = 16; /* avoid malloc(0) */
  if ((buf→mem = malloc(max)) == NULL) abort();
  buf→len = 0; buf→max = max;
  return buf;
}
buffer buf_addch(buffer buf, unsigned char c) {
  if (buf→max <= buf→len) {</pre>
    unsigned char *mem; size_t max = buf→max * 2;
    if ((mem = realloc(buf→mem, max)) == NULL)
      abort();
    buf→mem = mem; buf→max = max;
  }
  buf \rightarrow mem[buf \rightarrow len++] = c;
  return buf;
}
unit free_buffer(buffer buf) {
  free(buf→mem); free(buf);
  return tt;
}
```

```
Figure 26. buffer implementation in C.
```

# References

- [1] Abhishek Anand, Andrew Appel, Greg Morrisett, Zoe Paraskevopoulou, Randy Pollack, Olivier Savary Belanger, Matthieu Sozeau, and Matthew Weaver. 2017. CertiCoq: A verified compiler for Coq. In *The Third International Workshop on Coq for Programming Languages (CoqPL).*
- [2] Lars Birkedal, Mads Tofte, and Magnus Vejlstrup. 1996. From region inference to von Neumann machines via region representation inference. In Proceedings of the 23rd ACM SIGPLAN-SIGACT symposium on Principles of programming languages. 171–183. https: //doi.org/10.1145/237721.237771
- [3] Olivier Danvy and Lasse R Nielsen. 2005. CPS transformation of betaredexes. *Inform. Process. Lett.* 94, 5 (2005), 217–224. https://doi.org/10. 1016/j.ipl.2005.02.002
- [4] Cormac Flanagan, Amr Sabry, Bruce F Duba, and Matthias Felleisen. 1993. The essence of compiling with continuations. In Proceedings of the ACM SIGPLAN 1993 conference on Programming language design and implementation. 237–247. https://doi.org/10.1145/155090.155113
- [5] Xavier Leroy. 2009. Formal verification of a realistic compiler. Commun. ACM 52, 7 (2009), 107–115. https://doi.org/10.1145/1538788.1538814

- [6] Pierre Letouzey. 2003. A New Extraction for Coq. In Types for Proofs and Programs, Second International Workshop, TYPES 2002, Berg en Dal, The Netherlands, April 24-28, 2002 (Lecture Notes in Computer Science, Vol. 2646), Herman Geuvers and Freek Wiedijk (Eds.). Springer-Verlag. https://doi.org/10.1007/3-540-39185-1\_12
- [7] Eric Mullen, Stuart Pernsteiner, James R Wilcox, Zachary Tatlock, and Dan Grossman. 2018. Œuf: minimizing the Coq extraction TCB. In Proceedings of the 7th ACM SIGPLAN International Conference on Certified Programs and Proofs. 172–185. https://doi.org/10.1145/3167089
- [8] Gonzalo Navarro. 2016. Compact data structures: A practical approach. Cambridge University Press. https://doi.org/10.1017/ CBO9781316588284
- [9] Akira Tanaka, Reynald Affeldt, and Jacques Garrigue. 2018. Safe lowlevel code generation in Coq using monomorphization and monadification. *Journal of Information Processing* 26 (2018), 54–72. https: //doi.org/10.2197/ipsjjip.26.54
- [10] The Coq Development Team. 2020. The Coq Proof Assistant. https: //coq.inria.fr/.
- [11] The Coq Development Team. 2020. The Coq reference manual: Release 8.12.0. (2020).